

# INVENTOR INFORMATION

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# CORRESPONDENCE INFORMATION

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#### APPLICATION INFORMATION

Title Line One:: MANUFACTURING METHOD FOR SEMICONDUCTOR D  
Title Line Two:: EVICE  
Total Drawing Sheets:: 12  
Formal Drawings?: Yes  
Application Type:: Utility  
Docket Number:: 740756-2306  
Secrecy Order in Parent Appl.?: No

#### REPRESENTATIVE INFORMATION

Registration Number One:: 38285  
Registration Number Two:: 35483  
Registration Number Three:: 45285  
Registration Number Four:: 41345  
Registration Number Five:: 26477

#### PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 2000-131353  
Filing Date:: 04-28-2000  
Country:: Japan  
Priority Claimed:: Yes

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